

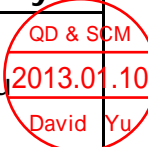




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## 2U12 XJ3000-2123 EOB Functional Test Report

Formal Release

Initiated by	Reviewed by	Approved by
 Jeffery Lu	 Tony Wang	 David Yu
Originate Date	Revision	Release Status
2013/1/2	A1	Formal Release

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# Revision History

## Revisions

REV.	DESCRIPTION	DATE	Engineer
A1	2U12 XJ3000-2123 EOB Functional Test Backplane B40-2AMCTMXX00A100 Rev:A1 Firmeare Version: 1.11.1.1 MFG Version: 1.1.1.6 1.Hardware Change SAS and Power of Bead,P/N from B40-2AMCTMXX00A000 to B40-2AMCTMXX00A100 2.Correct the zoning permission table,MFG Version from 1.1.1.5 to 1.1.1.6	2013/1/2	Jeffery Lu

If product change or information change/update, the report will be revised and released next edition.

### Date of Test:

Test Started	Test Completed
2012/12/24	2013/1/2

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# 1 Introduction

## 1.1 Scope

This document is for demonstrating product conformance in the Various Development Phases of a project.

## 1.2 Purpose

Provide a formal and consistent process for measuring and validation the reliability of a given design. Identify any design discrepancies or electrical, mechanical, firmware and system issues.

## 1.3 Reference Information

The following documents form a part of this test plan to the extent specified herein.

- DVT Requirements Document
- Current Hardware Platform Evaluation Test Plan

Owner	Document List Review
PM	Application form for DVT functionality validation
PM	Product specification
EE1	Product EVT test report
QT	Board level compatibility EVT test report
EE1	PCB Schematic / Layout

## 2 Plan of Action Reference

### 2.1 Plan of Action Procedure

- Refer to this document or other identified specification to start product testing.
- Identify all necessary requirements and equipment for the test.
  - All equipment must be calibrated on an annual basis. Documentation of the calibration must be available.
  - Proper maintenance of equipment is required.
- Complete testing according to instructions or procedures contained in this document.
- Identify whether or not product or product component passes or fails.
- Report all test results to DQA designated personnel and database.
  - Within the Problem Tracking System, the function test shall be referenced in the short description of the issue.
- The EE design teams have the responsibility to resolve all issues and concerns by PVT date.
- Identified issues and concerns will be worked in order of priority and resolved according to the mechanical checklist and any associated documented specifications.
- The QAE team may identify resolution for an issue regarding a product in the design process, if it is deemed necessary for the QAE team to be involved.

### 2.2 Test Reporting

Throughout the process of development, all progress in testing must be tracked and communicated to the DQA weekly.

Each test shall be tracked as follows:

Definition	Description
Pass	All units were able to complete testing within the specified Pass Criteria
Fail	JUT were not able to complete testing within the specified Fail Criteria
Bug	Unable to predict potential problems
Pending	Test initially failed but is able to pass after fixes were implemented

All Pass/Fail data results must be repeatable.

### 3. DUT Images



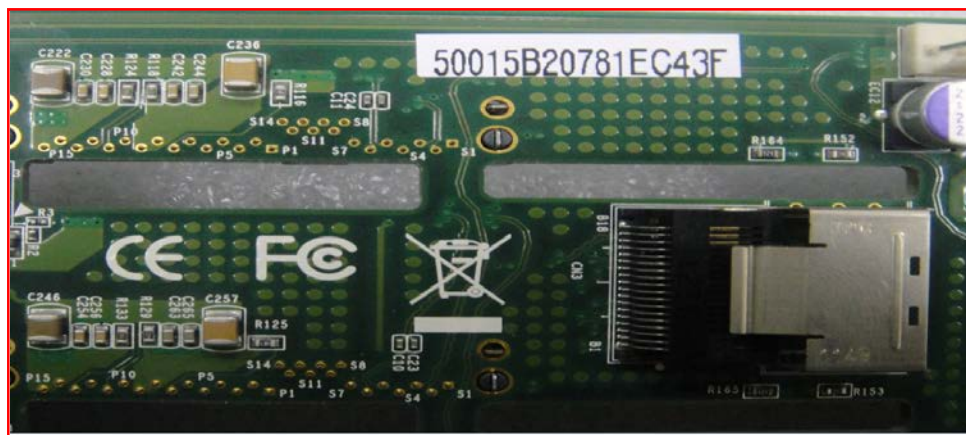
Front Angle



Rear Angle



SN: 505-12082300110081



PN: B40-2AMCTMXX00A100

SASADDRESS: 50015B20781EC43F

RD EE Rework:Change SAS and Power of Bead

Change P/N from B40-2AMCTMXX00A000 to B40-2AMCTMXX00A100

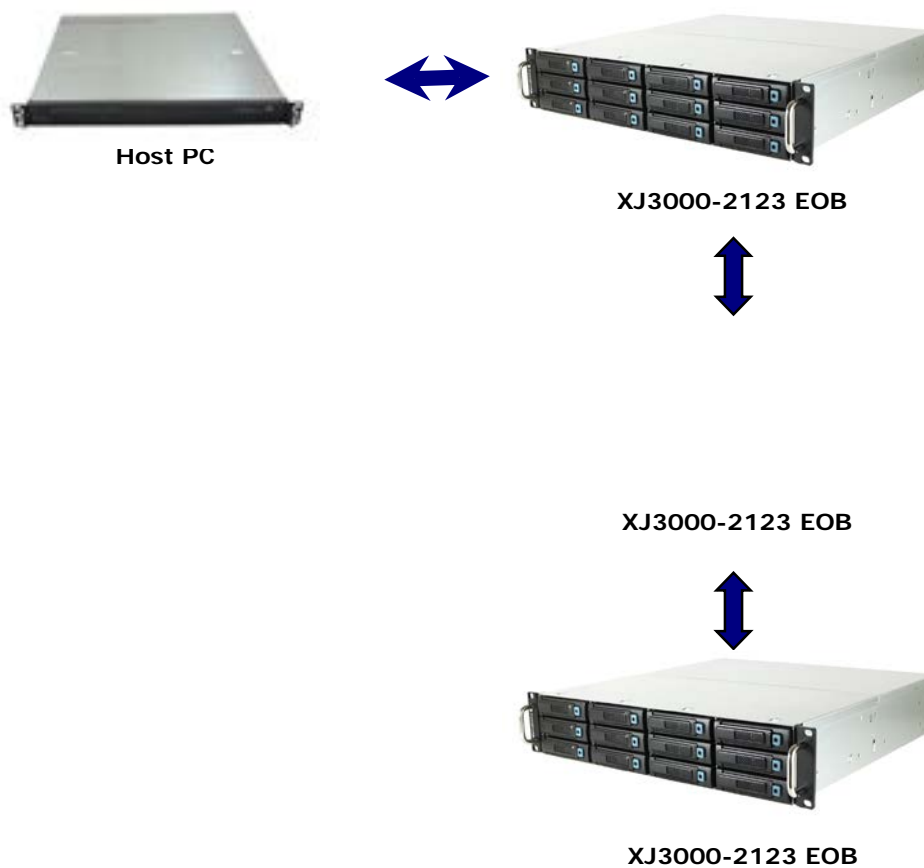
# 4 Target Device Configuration and Environment

## 4.1 Test Device Configuration Diagram

### 1. Single Host



### 2. Cascading



## 4.2 Table of Test Configuration

Host Configuration					
Item		Vender / Model		Detail	
Morthterboard		AIC Aquarius ver.D		Motherboard of the host	
Operation System		Microsoft Windows		Server 2003 Entrprise Edition	
CPU		Intel		Xeon 2.33GHz *1	
Memory		DSL		KVR66702S8P5 /512 1.8V *4	
Hard Disk Drive		Seagate / ST9160511NS		SATA HDD / 160GB *1	
HBA/ RAID Card Configuration					
Card	Vender / Model	Firmware ver.	BIOS ver.	Driver ver.	GUI Software ver.
HBA Card	LSI 9205-8e	14.00.00.00	07.27.00.00	2.0.55.0	NA
RAID Card	Intel RS2PI008	2.130.363-1846	3.25.00-4.12.05.00	5.2.103.0	12.05.03.00
XJ3000-4243S JBOD HDD Configuration					
Vender / Model		Interface		Detail	
WD/WDCW1003FBYX0		SATA 3G		1T	
Hitachi / HitachiHUS72403		SATA 6G		3T	
SEAGATE/ST3146854SS		SAS 3G		136G	
Hitachi / HUS154545VLS300		SAS 3G		450GB	
WD/WD1000FYYG01A21		SAS 6G		1T	
Hitachi / HUS156060VLS600		SAS 6G		600G	
SEAGATE/ST1000NM0001		SAS 6G		1T	

## 4.3 DUT Main Hardware Configuration

Item	Product Number	Quantity	Detail
Backplane Board	B40-2AMCTMXX00A100	1	2U12 XJ3000-2123 EOB 6G JBOD 505-12082300110081
Power Housing	Zippy MRW-5500V4V	1	AC INPUT:100-240V 47-63Hz 8-4A DC OUTPUT:500W
Power Module	Zippy MRW-3500V	2	AC INPUT:100-240V 47-63Hz 8-4ADC OUTPUT:500W
Repeater Board	B44-87XB4-XX00C010	1	Repeater Board 505-12070400110007



## 5 Functional Test

Test Engineer	Jeffery Lu				
Model Name	2U12 XJ3000-2123 EOB				
Firmware	1.11.1.1				
MFG	1.1.1.6				
Microchip PIC24 Firmware	11				
Backplane Board	B40-2AMCTMXX00A100	Rev	A1	S/N	505-12082300110081
RAID Card	Intel(R) RAID RS2PI008			Driver	5.2.103.0
Power Housing	Zippy MRW-5500V4V AC INPUT:100-240V 47-63Hz 8-4A DC OUTPUT:500W				
Power Module	Zippy MRW-3500V AC INPUT:100-240V 47-63Hz 8-4ADC OUTPUT:500W				

Item Test	Power Module		
Test Procedure		Result	
Hot swap the power module and power cord, confirm the right side of functions are work normally.	Hot-swap PSU under power on state, check fail led, beeper, and console status.		Pass
	Power cord interrupt, check fail led, beeper, and console status.		Pass
	PSU status under GUI		Pass
	PSU status under console		Pass

Item Test	Shake Test		
Test Procedure		Result	
When powering up the enclosure and then used SAS Cable plug in backplane connector, then gently shake SAS Cable connector, check if PHY status is normally.	Bend the SFF-8088 cable.		Pass
	Shaking cable around the junction.		Pass

Item Test	LED Color of Front Panel		
Test Procedure		Result	
Check LED color of front panel.	Power Fail LED		Blue
	Temperature Alarm LED		Red
	Fan Alarm LED		Red

<b>Item Test</b>	<b>System FAN</b>	
Test Procedure		Result
Hot swap or start/ stop the FAN module, and confirm the right side of functions are work normally.	How-swap fan under power on state, check fail led, beeper, GUI, and console status.	Pass
	Fan status under GUI	Pass
	Fan status under console	Pass
	Smart Fan, if temperature upgrade, the rotational speed of fan was increased.(depend on spec.)	Pass
<b>Item Test</b>	<b>Voltage Sensor</b>	
Test Procedure		Result
To check if value of the voltage from the specific function showing the status is ok.	Check voltage under Hyperterminal status.	Pass
	Check voltage under GUI	Pass
<b>Item Test</b>	<b>HDD Bays</b>	
Test Procedure		Result
Insert all HDDs into all disk bays, then hot swap some HDDs, and check all HDDs tray's activity/ data access LED,and RS-232 console status is normal. HDDs could be SAS/SATA interface. The right side of list are the HDD configuration.	SAS HDDs	Pass
	SATA HDDs	Pass
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Item Test	Expander	
Test Procedure		Result
Check channel of expander that function is ok.	Check PHY state and negotiated link speed, confirm the PHY contents conform with actual HDD configuration	Pass
	Up connector is substrate type.	Pass
	Down connectors were table type.	Pass
Item Test	SAS Zoning	
Test Procedure		Result
Applying SAS Zoning function to segment HDD group, and obtain benefit of dual host that could connect the same JBOD simultaneously.	Group8 and Group9 were run independently.	Pass
	Group1 can detect Group8 and Group9.	Pass
	Take HDD for Group8, and all HDD of Group8 could build RAID and run Iometer.	Pass
	Take HDD for Group9, and all HDD of Group9 could build RAID and run Iometer.	Pass
Item Test	Band Width	
Test Procedure		Result
Using SFF-8088 to connect dual external ports that were designed on the same RAID card, other side was connecting with JBOD.	When Iometer was keep on running, then remove one SFF-8088, the performance would be decreased, but RAID is still living.	Pass
	Although one SFF-8088 cable was removed, and RAID volume still lives	Pass
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Item Test	Temperature Sensor	
Test Procedure		Result
When temperature sensor $\geq$ designated degree, the GUI will spring up warning message, and fan speed from the RS232 console showing the status is normal.	T1, T2, warning, Alarm value configuration setting	Pass
	Temperature detected status under GUI	Pass
	Temperature detected status under Hyperterminal	Pass
	Break through T1 value, the rotational speed of fan was increased	Pass
	Break through alarm value, then the fail led will light up	Pass
	Break through alarm value, beep from buzzer	Pass
	Break through alarm value, RPM of fan is the highest	Pass
Item Test	Firmware upgrade	
Test Procedure		Result
To upgrade the firmware then check if upgrade successfully.	In band mode(debug port)	Pass
	Out-of-band mode(console port)	Pass
Item Test	Burn-in Test	
Test Procedure		Result
Using performance assessment tool, let JBOD status was maintain full loading on 24~72 hours. No any error message was showed.	Adjust conf. to 100% read	Pass
	Adjust conf. to 100% write	Pass
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Item Test	Mute Button	
Test Procedure		Result
When the warning sound was activated, press the mute button could stop this sound.	Hot swap the power module	Pass
	Hot swap the fan module	Pass
	Temperature was detected over default alarm value(over 55 degrees centigrade).	Pass
Item Test	Cascading	
Test Procedure		Result
To cascade the SAS JBOD device, check if Enclosure Name and SAS HDDs can be detect, create/ rebuild/ delete a RAID and doing I/O access is ok.	This XJ3000-2123 EOB cascaded ,A host cascaded this XJ3000-2123 EOB, then the XJ3000-2123 EOB layer by layer.(Total 3Layer)	Pass
Item Test	SES Lighting Signal	
Test Procedure		Result
To verify SES lighting signal, using sg3_utils tool to check lighting mode of each status is correct.	Request consistency check in progress	Pass
	Request in critical array	Pass
	Request in failed array	Pass
	Request rebuild/ remap	Pass
	Request rebuild/ remap aborted	Pass
	Request device missing indication	Pass
	Request insert	Pass
	Request removal	Pass
	Request identify	Pass
	Request fault indication	Pass
Item Test	SFF-8088 External Port	
Test Procedure		Result
Check SFF-8088 external ports from JBOD were 3G/S connector or 6G/S connector.	Up port	6G/S
	Down port	6G/S
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## 6 RAID Card Test

Test Engineer	Jeffery Lu				
Model Name	2U12 XJ3000-2123 EOB				
Firmware	1.11.1.1				
MFG	1.1.1.6				
Microchip PIC24 Firmware	11				
Backplane Board	B40-2AMCTMXX00A100	Rev	A1	S/N	505-12082300110081
RAID Card	Intel(R) RAID RS2PI008			Driver	5.2.103.0
HDD Type	WD/WDCW1003FBYX0 Hitachi / HitachiHUS72403 SEAGATE/ST3146854SS Hitachi / HUS154545VLS300 WD/WD1000FYYG01A21 Hitachi / HUS156060VLS600 SEAGATE/ST1000NM0001				
Power Housing	Zippy MRW-5500V4V AC INPUT:100-240V 47-63Hz 8-4A DC OUTPUT: 500W				
Power Module	Zippy MRW-3500V AC INPUT:100-240V 47-63Hz 8-4ADC OUTPUT: 500W				

RAID Function	Test Procedure	Result
Intel(R) RAID RS2PI008 RAID Function	Create/Remove a RAID 0 volume	Pass
	Create/Rebuild/Remove a RAID 1 volume	Pass
	Create/Rebuild/Remove a RAID 5 volume	Pass
	Create/Rebuild/Remove a RAID 6 volume	Pass
	Create/Remove a RAID 00 volume	Pass
	Create/Rebuild/Remove a RAID 10 volume	Pass
	Create/Rebuild/Remove a RAID 50 volume	Pass
	Remove a crashed RAID 0 volume	Pass
	Remove a crashed RAID 1 volume	Pass
	Remove a crashed RAID 5 volume	Pass
	Remove a crashed RAID 6 volume	Pass
	Remove a crashed RAID 00 volume	Pass
	Remove a crashed RAID 10 volume	Pass
	Remove a crashed RAID 50 volume	Pass

## 7 HBA Card Test

Test Engineer	Jeffery Lu				
Model Name	2U12 XJ3000-2123 EOB				
Firmware	1.11.1.1				
MFG	1.1.1.6				
Microchip PIC24 Firmware	11				
Backplane Board	B40-2AMCTMXX00A100	Rev	A1	S/N	505-12082300110081
RAID Card	LSI 9205-8e			Driver	2.0.55.0
HDD Type	WD/WDCW1003FBYX0 Hitachi / HitachiHUS72403 SEAGATE/ST3146854SS Hitachi / HUS154545VLS300 WD/WD1000FYYG01A21 Hitachi / HUS156060VLS600 SEAGATE/ST1000NM0001				
Power Housing	Zippy MRW-5500V4V AC INPUT:100-240V 47-63Hz 8-4A DC OUTPUT: 500W				
Power Module	Zippy MRW-3500V AC INPUT:100-240V 47-63Hz 8-4ADC OUTPUT: 500W				
Other					

HBA Function	Test Procedure	Result
LSI 9205-8e SAS HBA Card	While using LSI 9205-8e SAS HBA to connect with XJ3000-2123 EOB on Board SAS JBOD. Then check if all hard drives can be detect by LSI BIOS utility.	Pass
	While using LSI 9205-8e SAS HBA to connect with XJ3000-2123 EOB on Board SAS JBOD. Then check if all hard drives can be detect by LSI MSM.	Pass
	While using 9205-8e SAS HBA to connect with XJ3000-2123 EOB on Board SAS JBOD. Then check if all hard drives can be detect by OS Disk management.	Pass

## 8 Basic PSU Verification

Model Name	2U12 XJ3000-2123 EOB
Power Housing	Zippy MRW-5500V4V AC INPUT: 100-240V 47-63Hz 8-4A DC OUTPUT: 500W
Power Module	Zippy MRW-3500V AC INPUT: 100-240V 47-63Hz 8-4ADC OUTPUT: 500W

Item	Comment		Result
Alarm Reset Button	Hot swap the PSU0's power cord and check if audio alarm (buzzer sound)		Pass
	Hot swap the PSU1's power cord and check if audio alarm (buzzer sound)		Pass
Check Output Voltage	Output Voltage	SPEC.	
	+5V	4.75V ~5.25V	5.104V
	+12V	11.4V~12.60V	12.10V
	-12V	-11.4V ~ -12.60V	n/a
	-3.3V	3.13V~3.47V	n/a
	+5VSB	4.75V ~5.25V	n/a
Short Circuit	+3.3V	The power supply shall be latched in case any short circuit is taken place at +12V,-12V,+3.3V,+5V output	n/a
	+5V		Pass
	-12V		n/a
	+12V		Pass



## 9 Summary

Item	Descriptions	Result
Enclosure Function Test	Power Module	Pass
	Shake Test	Pass
	LED Color of Front Panel	Pass
	System FAN	Pass
	Voltage Sensor	Pass
	HDD Bays	Pass
	Expander	Pass
	SAS Zoning	Pass
	Band Width	Pass
	Temperature Sensor	Pass
	Firmware upgrade	Pass
	Burn-in Test	Pass
	Mute Button	Pass
	Cascading	Pass
	SES Lighting Signal	Pass
	SFF-8088 External Port	Pass
RAID Function Test	Intel RS2PI008 SAS RAID CARD	Pass
HBA Function Test	LSI 9205-8e SAS HBA	Pass

\*\* Notes: Test items and test contents depend on spec.

# Bug List

## Class

A	Major
B	Minor
C	Limitation
D	Other(Suggestion)

NO.	Date	Class	Bug/Limitation/Suggestion	Initiator	Status	Solution
1	2012/12/28	A	做Phyzone 時會無法看到HDD(Firmware: 1.11.1.1,Mfg:1.1.1.5)	Jeffery	Close	Update Firmware: 1.11.1.1, Mfg:1.1.1.6 Re-test ok